

PLANAR FILTER AND MULTI-POLE ANGLE-CONNECTING  
DEVICE WITH A PLANAR FILTER

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Cross-Reference to Related Applications:

This application is a Continuation-In-Part of U.S. Application  
No. 09/479,022, filed January 7, 2000.

10 Background of the Invention:

Field of the Invention:

The invention relates to a planar filter for plug-in  
connectors having a multiplicity of signal pins to be  
connected that are disposed in rows and columns, a carrier  
which has an opening for each of the signal pins, and a  
capacitor in the vicinity of each of the openings with a first  
layer connected to an assigned signal conductor, a second  
layer for connection to ground and a dielectric in the form of  
a layer between the first layer and the second layer. The  
20 invention also relates to a multi-pole angle-connecting device  
with a planar filter.

In the case of multi-pole plug-in connectors, which are used  
for the transmission of digital or analog measuring signals  
25 from multi-function measuring devices or in high-speed  
transmission of information, there is the necessity for

filtering in order to filter out interference signals that are introduced. That filtering-out of interference signals which are introduced generally takes place with capacitors, of which there is one provided for each line carrying a signal. For  
5 that purpose, the capacitors are advantageously combined into planar filters and inserted into the plug-in connectors. The planar filters are passed through by the signal conductors and there is at least one capacitor provided for each of the signal conductors. The capacitors are disposed on a carrier, that is generally formed of alumina. French Patent 2 422 268  
10 describes such a plug-in connector with filtering for each connector pin. The connectors are inserted in a ceramic sheet provided with holes and are carried by that sheet. Each capacitor configuration includes a small dielectric ceramic sheet, which for its part is provided on one side with grounding electrodes that are provided throughout the surface with the exception of regions of the pin lead-throughs, and on the other side with insular signal electrodes which are electrically connected to the connector pins. The coating of  
20 the electrodes takes place, as is known from U.S. Patent No. 4,007,296, by the screen-printing process, with the customary pastes based on noble metals (including palladium among others). The grounding electrodes change into lateral metallizations, which are connected to the metallic housing  
25 and represent a ground discharge for the capacitors of the signal electrodes to the connector housing, and then on to the

connected device. Another filter is formed by either a ground electrode with clearances for the connector pins or a number of signal electrodes corresponding to the number of connector pins being applied to the carrier. A dielectric layer is applied on top thereof and finally the other electrode layer, that is to say the number of signal electrodes corresponding to the number of connector pins or a ground electrode with clearances for the connector pins, is applied on top thereof (see European Patent 0 124 264, U.S. Patent No. 3,267,342 and U.S. Patent No. 3,544,434). That configuration is then protected from external influences by a coating of a resistant material, for instance a lacquer. However, in that case too the carrier is formed by an alumina material, to which the electrodes are applied, generally through the use of a screen-printing technique, with a dielectric layer lying between. In the case of those filters, the conductive layers and the dielectric layers are applied in the manner of a "sandwich" through the use of a screen-printing technique, which causes problems in terms of the dielectric strength due to the thin dielectric layers. Since neighboring signal electrodes are disposed at only a small distance from one another, a capacitive coupling through unavoidable cross-capacitances cannot be ruled out. That coupling is extremely minor. However, in the case of the known planar capacitors, the mutually facing end surfaces of the signal electrodes are extremely small because of their small layer thickness and the

layer thicknesses of the dielectric layers are similarly extremely small, so that the resultant cross-capacitance is extremely small and the problem of crosstalk becomes insignificant. The production of such planar filters also requires an alternating application of metallic layers and dielectric layers and is therefore (relatively) complex. Furthermore, such planar filters lack dielectric strength. In order to improve the dielectric strengths, thicker dielectric layers are necessary. Then, however, the cross-capacitances that are decisive for a coupling assume values which no longer rule out crosstalk. Such a configuration, with capacitors of high dielectric strength formed as classic sheet capacitors, in which the parasitic capacitances can form and in certain cases can disturb signal transmission, is described in German Utility Model 297 12 001.8.

#### Summary of the Invention:

It is accordingly an object of the invention to provide a planar filter and a multi-pole angle-connecting device with a planar filter, which overcome the hereinafore-mentioned disadvantages of the heretofore-known devices of this general type, in which parasitic capacitances between signal electrodes are suppressed while maintaining dielectric strength, which can be produced cost-effectively and easily and which can be used reliably.

With the foregoing and other objects in view there is provided, in accordance with the invention, in a plug-in connector having signal conductors, a multiplicity of signal pins to be connected, the signal pins disposed in rows and columns, and a carrier having openings formed therein each for receiving a respective one of the signal pins, a monolithic planar filter, comprising capacitors each disposed in the vicinity of a respective one of the openings, the capacitors each having a first layer connected to an assigned one of the signal conductors, a second layer for connection to ground, and a dielectric carrier in the form of a layer disposed between the first and second layers, the carrier having two side surfaces, an edge and pin lead-throughs for the signal pins, and the carrier formed of a mass with a relatively high dielectric constant shaped into a block, perforated and subsequently sintered and ground; the ground electrode applied to and entirely areally covering one of the side surfaces of the carrier apart from the pin lead-throughs and a directly surrounding area, and the signal electrodes applied to the other of the side surfaces of the carrier, extending from the pin lead-throughs and forming insular regions extending substantially from the signal pins to the edge of the carrier.

According to the invention, the planar filter has a monolithic structure and the carrier is a mechanically inflexible and

rigid layer of a ceramic mass with a relatively high dielectric constant.

In accordance with another feature of the invention, ceramic masses which are suitable are those with a dielectric constant of not less than 5,000. Such masses are, for example, barium titanate masses. The dielectric constant is advantageously greater than 10,000.

10 The plate which is formed from this mass is provided with the holes necessary for leading through the signal pins, fired and sintered. After the thermal treatment, the plate which is thus obtained is ground to achieve planarity. The thickness of the carrier in this case ensures its dielectric strength. The capacitors for each of the pin lead-throughs are formed by signal electrodes assigned to them and a ground electrode.

One side of this sheet of the ceramic carrier that is prepared in this way bears the ground electrode and the other side is provided with the signal electrodes of the capacitors, which is applied with conductive printing pastes by screen printing. In this case, the ground electrode can be led at least up to one of the outer narrow sides of the carrier, for establishing a connection to the ground-housing of the plug-in connector.

Each signal electrode which surrounds a lead-through opening for the assigned connector pin may have been led into the lead-through, which provides an improved possibility for soldering with the connector pin concerned. Since the ground  
5 electrode covers the entire surface area of one of the side surfaces of the carrier, apart from the locations where the signal pins penetrate and their directly surrounding area, good shielding is achieved. The signal electrodes disposed on the other side surface of the carrier form insular regions,  
10 which extend from the locations where the signal pins penetrate, essentially surround the signal-pin lead-through and extend laterally to the edge of the carrier.

In accordance with a further feature of the invention, in order to obtain as smooth a surface as possible and consequently precisely defined electrical conditions, both side surfaces are lapped to planarity before applying the layers of the capacitors. This surface, which is thus particularly smooth and level, ensures a field-strength  
20 distribution undisturbed by deviating geometrical conditions.

Since the individual signal electrodes are opposite one another merely at their end surfaces, which account for negligible proportions of the surface area, the parasitic  
25 capacitances causing a capacitive coupling between the signal conductors are also negligible. As a result, a capacitive

coupling due to cross-capacitances or parasitic capacitances is at least reduced, if not ruled out entirely, so that crosstalk phenomena during analog transmissions are suppressed.

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In accordance with an added feature of the invention, a ceramic on a titanate basis is advantageously provided as the mass for this dielectric. Titanates of alkaline earth metals, for instance barium, strontium or a mixture thereof, are used in particular therefor. These masses, sintered as ceramic, have adequate mechanical strength and have higher dielectric constants, so that the desired capacitances can be achieved even with greater thicknesses of the carrier.

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With the objects of the invention in view there is also provided a multi-pole angle-connecting device, comprising the signal conductors each being continuous, assigned to a respective pole and disposed in a configuration; the lead-through openings in the carrier of the planar filter disposed in a configuration; a supporting plate, particularly in the form of a printed circuit board, having soldering points and lead-through openings in a configuration corresponding to one of the configuration of the lead-through openings and the configuration of the signal conductors; and the planar filter according to claim 1 having a side to be soldered onto and areally supported by the supporting plate; the signal

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device with a planar filter, it is nevertheless not intended to be limited to the details shown, since various modifications and structural changes may be made therein without departing from the spirit of the invention and within  
5 the scope and range of equivalents of the claims.

The construction and method of operation of the invention, however, together with additional objects and advantages thereof will be best understood from the following description  
10 of specific embodiments when read in connection with the accompanying drawings.

Brief Description of the Drawings:

Fig. 1 is a diagrammatic, perspective view of a signal-electrode side of a planar filter;

Fig. 2 is a perspective view of a ground-electrode side of a planar filter;

20 Fig. 3 is a fragmentary, perspective, sectional view of a planar filter, taken through a row of pin lead-throughs;

Fig. 4 is a partly broken-away and partly sectional view of an angle-connecting device with a supported planar filter;

25 Fig. 5 is an exploded view of the planar filter; and

Fig. 6 is a partial sectional view of the planar filter.

Description of the Preferred Embodiments:

Referring now to the figures of the drawings in detail and  
5 first, particularly, to Figs. 1-3 thereof, there is seen a  
carrier 1 of a planar filter generally marked by the reference  
number 10 which is formed by a block of an alkaline earth  
metal titanate, for instance barium titanate, strontium  
titanate or barium-strontium titanate, in which an excess of  
10 barium promises better results. This block, which is shown as  
being rectangular herein, with pin lead-throughs 2 for pins  
2', has been shaped and sintered and at least finish-ground,  
in order to achieve a level surface. Signal electrodes or  
first layers 3, which are applied to one side surface of this  
carrier or dielectric layer 1, are led up to and preferably  
15 even into, openings of the respectively assigned pin lead-  
throughs 2. Another side surface of the carrier or dielectric  
layer 1 is covered throughout with a ground electrode or  
second layer 4, in such a way that the pin lead-throughs 2 and  
20 a directly surrounding area having been left exposed. This  
ground electrode 4 is connected through the use of a silver or  
palladium conductive adhesive when the planar filter is  
inserted into a non-illustrated housing with a metallic mating  
piece. In this way an electrical ground connection is  
25 established.

The surface area of the signal electrodes 3, which are shown herein as being rectangular, together with the thickness of the carrier 1 and the dielectric constant of its material, determine the capacitance of the filter capacitors. Changing the surface areas of the individual signal electrodes 3 allows different capacitances to be established for the individual signal conductors connected to the pins 2'. The overall capacitance of a plug-in connector provided with such planar filters can consequently be achieved or set by the number of individual planar filters constructed in this way and by varying the size of the surface area of the signal electrodes.

The ground electrode 4, which is shown as being provided throughout, covers (virtually) the entire surface area of the carrier 1, apart from lead-through clearances 5. The lead-through clearances 5 ensure that short-circuits or flashovers between the pins 2' led through the pin lead-throughs 2 of the led-through signal conductors and the ground electrode 4 are prevented. This ground electrode 4 is shown herein as not being directly at the edge of the side surface of the carrier 1. It goes without saying herein that this metallic layer may also be taken up to at least one of the edge surfaces, for establishing the ground connection to the housing of a plug-in connector.

Fig. 4 shows an advantageous application of a supported filter 10 in a plug-in connector 15 with a plug housing 16 and plug pins 17 which are held through the use of an insulating pin holder 18. A supporting plate 11, such as a printed circuit board, a substrate or the like, is provided with lead-through openings 11.1 for a number of signal conductors 12 and 13 (as well as an undesignated signal conductor indicated by dashed lines in the case of 3-row plug-in connectors). The lead-through openings 11.1 are provided in such a way that a pattern of these openings 11.1 coincides with a pattern of openings in the carrier of the planar filter 10. The supporting plate 11 can be placed against the planar filter 10 to bear with surface-area contact. The openings 11.1 are metallized at least at edges and preferably in the lumen as well. The planar filter 10 can thus be inserted together with the supporting plate 11 (for example into a plug-in connector 15). The signal conductors 12 and 13 passing through the supporting plate 11 and the planar filter 10 are soldered to the metallizations in the region of the openings 11.1 in the supporting plate 11. Soldering joints 14 fix the signal conductors 12 and 13 on the supporting plate 11. In order to solder with the signal electrodes 3 (Fig. 2) of the planar filter 10 as well, it is adequate if the openings 11.1 are formed in the supporting plate 11 in such a way that the solder 14.1 is drawn by capillary action into a gap between an inner wall surface of the openings 11.1 and an outer surface

of the respective signal conductors 12 and 13. The solder is thus led through the individual openings 11.1, provided with the assigned conductor of the signal conductors 12 and 13, up to the assigned, exposed signal electrode 3. Electrical

5 connection of the signal conductors 12 and 13 to the assigned electrode of the signal electrodes 3 is thus ensured. This structure has the advantage of ensuring that the signal conductors 12 can be initially installed in their extended form. Subsequent bending in desired directions no longer  
10 mechanically stresses the planar filter 10.

An embodiment of the configuration as shown in Fig. 4 is illustrated in detail in Figs. 5 and 6. This configuration is a combination of a planar filter 10 and a support plate 11.  
15 Figs. 5 and 6 show exploded views of the planar filter 10 and the support plate 11 to illustrate their components more clearly.

The embodiments of the planar filter 10 and the support plate  
20 11 shown in Figs. 5 and 6 are intended for use with the 9-pin plug-in connector. However, an embodiment for a 15-pin connector could be constructed and would have features similar to those shown in Figs. 1-3 wherein the signal electrodes 3 and ground electrode 4 have increased height.

While Fig. 5 is an exploded view, the supporting plate 11 actually directly attaches to the planar filter 10 (see Figs. 4 and 6). The supporting plate 11 is provided with pin lead-throughs 11.1 that are configured to overlay the pin lead-throughs 2 of the planar filter 10, respectively. The pin lead-throughs 11.1 are aligned with the pin lead-throughs 2 of the planar filter 10. The pin lead-throughs 11.1 are metallized (edges as well as inner walls) and are configured such that a gap is formed between the inner walls of each of the pin lead-throughs 11.1 and their respective pins 13 being led through the corresponding lead-through 11.1. The edges of the pin lead-throughs 11.1 are preferably provided with chambers. When being built, the supporting plate 11 and the planar filter 10 are placed together so that the supporting plate 11 is lying directly against the side wall of the planar filter 10 bearing the signal electrodes 3 so that the signal pins 13 are lead through the pin lead-throughs 11.1. In this position, the configuration is soldered so that the solder 14 joins each of the pins 13 to the corresponding metallized pin lead-through 11.1 and that the solder 14 is drawn by capillary forces into the gap to the signal electrodes 2 so that an electrical connection to the signal electrodes 2 arises. This configuration forms a compact block that can be handled in an easy manner.